AMENDMENTS TO THE SPECIFICATION:

Please amend the paragraph beginning on page 10, line 8 and ending on page 10, line 17 as follows:

—The key bits are incorporated into the circuit in such a way that there are no equivalent keys, i.e., that different combinations of the key bits give rise to different reversible transformations. This is not a problem for checking since the parameters n and k are small. For each fixed key, such reversible transformations are affine, and the non-linearity is achieved by the key bits depending on the control input data bits. For [[n=3]] n=2 note that all 24 reversible transformations of 2 input bits are necessarily affine. —